

What is claimed is:

1. An image sensing apparatus characterized by comprising:

a plurality of photodetectors;

5           resettable integrators which are arranged for said respective photodetectors, integrate charges output from said photodetectors, and output voltage signals corresponding to integrated charge amounts;

10           output switches arranged on output sides of said respective integrators to connect said integrators to an external output line;

15           a first switch series-inserted between each photodetector and each integrator; and

20           a controller for closing said first switch when an absolute value of an output voltage from said integrator is lower than a predetermined reference voltage, and opening said first switch when the absolute value of the output voltage from said integrator is not lower than the predetermined reference voltage.

25           2. An apparatus according to claim 1, characterized in that said apparatus further comprises a second switch for connecting each photodetector and an overflow drain for removing charges output from said photodetector, and

30           said controller opens said second switch when the absolute value of the output voltage from said integrator is lower than the predetermined reference voltage, and closes

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said second switch when the absolute value of the output voltage from said integrator is not lower than the predetermined reference voltage.

3. An apparatus according to claim 1, characterized in that said apparatus further comprises:

a third switch series-inserted between each integrator and each output switch; and

a fourth switch for connecting a terminal of said output switch on the integrator side and a supply source for supplying the predetermined reference voltage, and

said controller closes said third switch and opens said fourth switch when the absolute value of the output voltage from said integrator is lower than the predetermined reference voltage, and opens said third switch and closes said fourth switch when the absolute value of the output voltage from said integrator is not lower than the predetermined reference voltage.